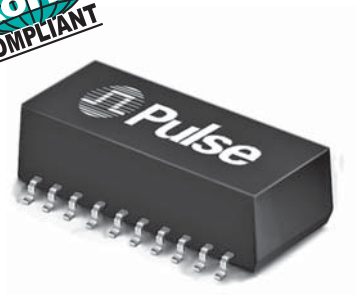
ISDN S-INTERFACE MODULES
Surface Mount, Dual, 1500 Vrms



- RoHS peak reflow temperature rating 245°C
- Meets the pulse waveform template of CCITT I.430 when recommended transformer and chip pair are used
- Developed for enhanced EMC performance
- Excellent longitudinal balance
- Low- or high-frequency choke options available

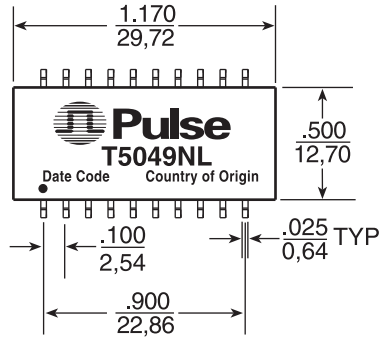
Electrical Specifications @ 25°C — Operating Temperature 0°C to 70°C

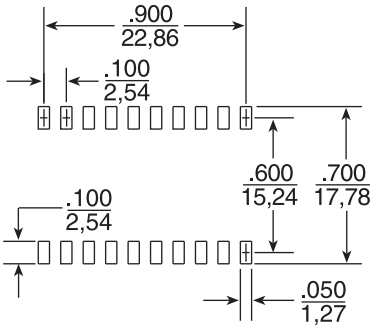
RoHS Compliant Part Number	Ratio (± 2%)	OCL Pri (mH MIN)	LL Sec (µH MAX)	Cw/w (pF MAX)	CD Pri (pF MAX)	DCR Pri (Ω +25% MAX)	DCR Sec (Ω +25% MAX)	D IDC (mA MAX)	Isolation Voltage (Vrms MIN)	Δ IDC (mA MAX)	Secondary Pins
	Transformers							Chokes			
T5049NL	1CT:1CT	30	10	150	100	3.4	3.4	4.7 mH	1.4	3	20-18, 13-11

NOTE: When ordering Tape & Reel packaging, add a "T" suffix to the part number (ex: T5049NLT).

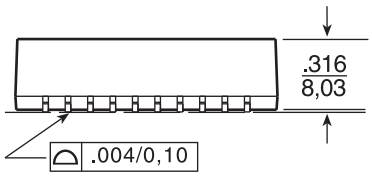
Mechanical

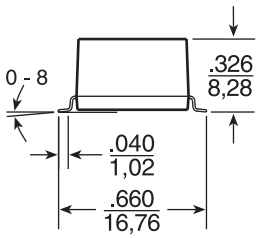
T5049NL



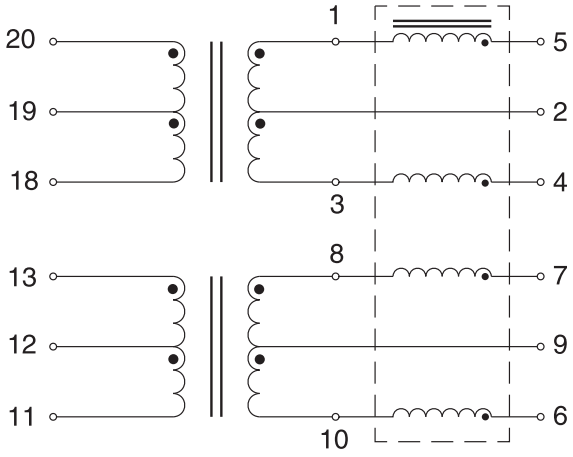


SUGGESTED PAD LAYOUT





Schematic



Weight . . . . . **6.94 grams**
Tape & Reel . . . . . **.250/reel**
Tube **15/tube**

Dimensions: Inches / mm

Unless otherwise specified, all tolerances are ± .010 / 0,25

ISDN S-INTERFACE MODULES
Surface Mount, Dual, 1500 Vrms

Pulse
A TECHNITROL COMPANY

Module Selection Guide		
IC Manufacturer	**IC Part Number**	**Pulse Part Number**
Siemens	PEB2080/2081/2084/2085/2086 PSB 2186 PSB 21381/21382/21383/21384	T5049NL

Definition of Terms

Ratio: This is the turns ratio, expressed as "Primary:Secondary". The term "CT" designates a center-tapped winding.

OCL: Open Circuit Inductance, measured 20 kHz, 100 mV.

L_L Sec: Leakage Inductance measured across the primary with the respective secondary winding short-circuited.

$C_{W/W}$: Winding capacitance, formed by the primary and secondary wire. These wires form the "Plates" of this capacitor. Measured at 100 kHz, 20 mV.

CD Pri: This is the distributed capacitance.

DCR: This is the resistance of the windings when measured in DC conditions.

ΔI_{DC}: The maximum specified unbalanced DC current capability of the device.

The minimum primary inductance and the maximum distributed capacitance satisfy the transmitter output and receiver input impedance requirements of CCITT I.430 for both TE and NT.

The maximum distributed capacitance allows sufficient margin for the capacitance of the IC and a protection diode network. It is consistent with the overall maximum value specified and permitted length of the basic access TE cord.

Flammability – Materials used in the products are recognized UL94-VO. Products meet the requirements of IEC 695-2-2 (needle flame test).

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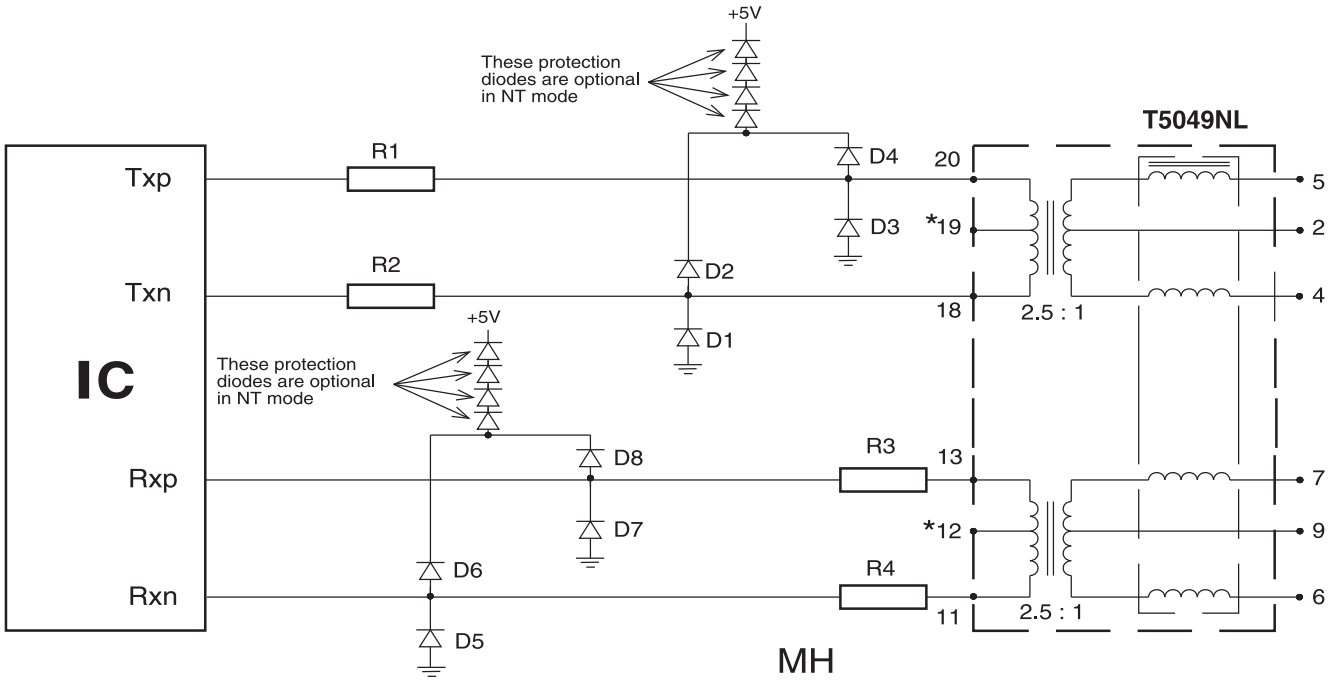
Surface Mount, Dual, 1500 Vrms

Application Notes

The S-Interface is the standardized four wire digital telephone access point defined by the CCITT I-Series recommendations for the Integrated Service Digital Network. This "basic rate access" accommodates two 64 Kbps "B-channels" for information, one 16 Kbps "D-channel" intended for signaling and control, and 48 Kbps for framing and other purposes, giving a total rate of 192 Kbps. The CCITT physical layer recommends that the user network interface be transformer coupled as shown in our typical application notes.

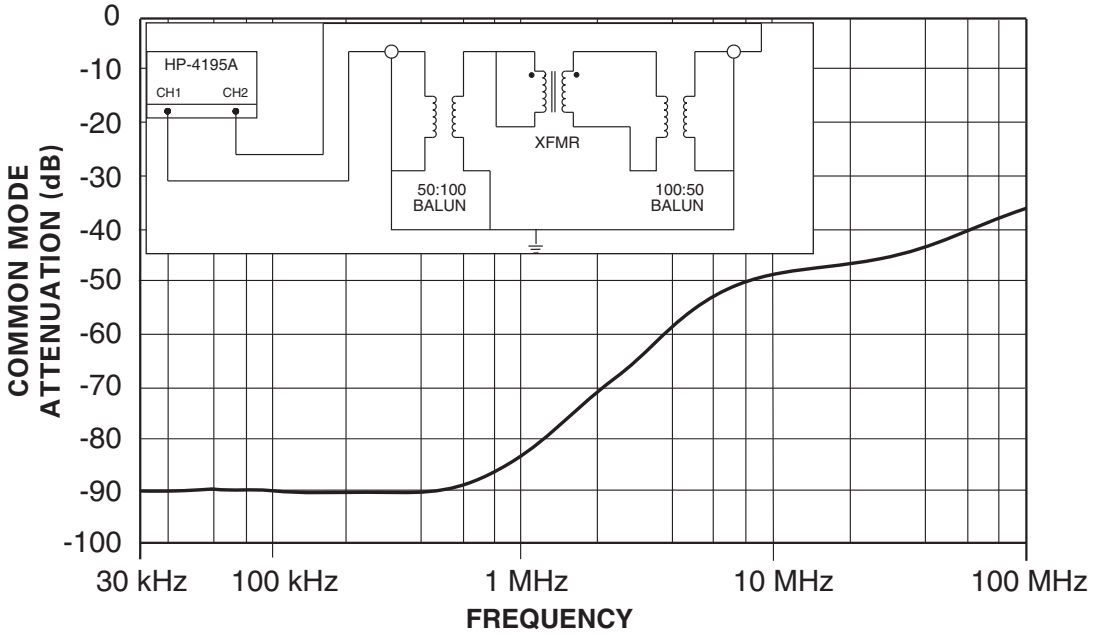
Typical S-Interface Application Circuit for Motorola MC145574



***NOTE:** Refer to Silicon Vendors' Application Notes for more details on power supply connection and specific component values.

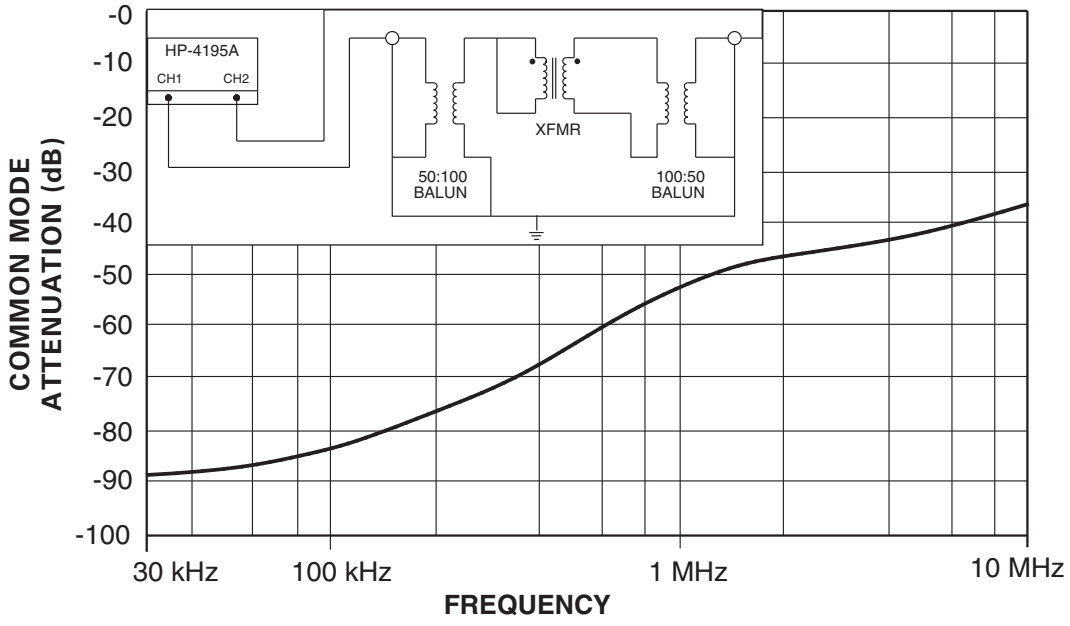
Common Mode Choke Performance

Typical Common Mode Attenuation for the high-frequency Common Mode Choke (470 µH) based on a 50 Ω system.



Common Mode Choke Performance *(continued)*

Typical Common Mode Attenuation for the low-frequency Common Mode Choke (4.7 mH), based on a 50 Ω system.



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